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School of Engineering Virginia Commonwealth University

RJ Mattauch, DF Boudinot, GS Dean - etd.vcu.edu

... **Bluetooth, Infrared** etc. it has become essential to develop applications that suit the various types of hardware that support such wireless technologies. ...

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Technical Report

U Saif - cl.cam.ac.uk

Page 1. Technical Report Number 527 Computer Laboratory UCAM-CL-TR-527 ISSN

1476-2986 Architectures for ubiquitous systems Umar Saif January 2002 ...

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A high performance, versatile residential gateway

JA Sanchez-P, K Pramataris, N Vogiatzis - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... is not mature enough yet and **Infrared** is expected ... HomePNA and RF with 802.11 and **Bluetooth** will compete ... inter- faces for particular equipment (eg **PC** cards for ...

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WIRELESS HOME NETWORKING

D Hepper, O Klank - [ifn.ing.tu-bs.de](http://ifn.ing.tu-bs.de)

... these: from TV-based to **PC**-based multimedia ... Whether **infrared** transmission can be an alternative or ... 2. **Bluetooth** Special Interest Group, 1999: [http:// www ...](http://www...)

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HomeRF: wireless networking for the connected home

KJ Negus, AP Stephens, J Lansford - IEEE Personal Communications, 2000 - [cs.cmu.edu](http://cs.cmu.edu)

... CP USB **PC** ... **Bluetooth** 1 Mb/s Medium Via PPP Via IP & Cellular < 10 m < 1 mA ... it is still very expensive compared to the very low cost of **Infrared** Data Association ...

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The Design And Implementation Of HomeRF: A Radio Frequency Wireless Networking Standard For The ...

J Lansford, P Bahl - Proceedings of the IEEE, 2000 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... 2 Very Fast **Infrared** [8] at 16 Mb/s is a ... data networking is available even when the **PC** is inoperative. ... TCP/IP protocol [19] stack within a **host** platform; the ...

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JAVA AND JINI TECHNOLOGY APPLIED TO ROBOT MOVEMENTS CONTROL Oleg Starostenko, Jose Chavez A., Alfred ...

BUA de Puebla - [mail.udlap.mx](mailto:mail.udlap.mx)

... Other complimentary network technologies, such as **Bluetooth**, JetSend, and ... pixels) and conditions of eight **infrared** sensors where ... of the system the **PC** an **SUN** ...

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Drabu, Yasir

H Peyravi - [cs.kent.edu](http://cs.kent.edu)

... 1 Mb/s (only), Direct Sequence 1 and 2 Mb/s and diffuse **infrared**. ... is designed to carry ATM cells, but also IP packets, Firewire packets (**IEEE 1394**) and digital ...

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The design and technological feasibility of home systems for the elderly

C Tweed, G Quigley - The Queens University, Belfast, 2000 - [qub.ac.uk](http://qub.ac.uk)

... 10 4.1.1 **PC** processors ... 24 5.3 FireWire (**IEEE 1394**).... 25 5.4 ...

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F Moili - [cs.tcd.ie](http://cs.tcd.ie)

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## A MOBILE CODE APPROACH TO INTERACT WITH UBIQUITOUS EMBEDDED SYSTEMS

K Kangas, J Roening - [student.oulu.fi](#)

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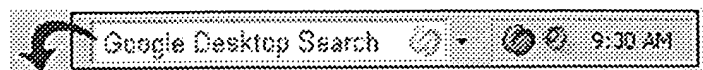
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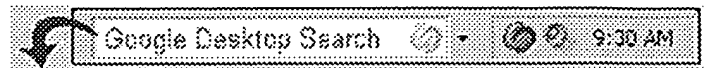
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### 1 [A generic architecture for on-chip packet-switched interconnections](#)



Pierre Guerrier, Alain Greiner

 January 2000 **Proceedings of the conference on Design, automation and test in Europe**

Publisher: ACM Press

Full text available: pdf(100.74 KB)


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### 2 [MemorIES: a programmable, real-time hardware emulation tool for multiprocessor server design](#)



Ashwini Nanda, Kwok-Ken Mak, Krishnan Sugavanam, Ramendra K. Sahoo, Vijayaraghavan Soundararajan, T. Basil Smith

 November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11

Publisher: ACM Press

Full text available: pdf(1.84 MB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Modern system design often requires multiple levels of simulation for design validation and performance debugging. However, while machines have gotten faster, and simulators have become more detailed, simulation speeds have not tracked machine speeds. As a result, it is difficult to simulate realistic problem sizes and hardware configurations for a target machine. Instead, researchers have focussed on developing scaling methodologies and running smaller problem sizes and configurations that atte ...

### 3 [Estimation and design techniques for energy-efficient memory systems: Energy-efficient flash-memory storage systems with an interrupt-emulation mechanism](#)



Chin-Hsien Wu, Tei-Wei Kuo, Chia-Lin Yang

 September 2004 **Proceedings of the 2nd IEEE/ACM/IFIP international conference on Hardware/software codesign and system synthesis**

Publisher: ACM Press

Full text available: pdf(179.27 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

One of the emerging critical issues for flash-memory storage systems, especially on the implementations of many embedded systems, is on its programmed I/O nature for data transfers. Programmed-I/O-based data transfers might not only result in the wasting of valuable CPU cycles of microprocessors but also unnecessarily consume much more energy from batteries. This paper presents an interrupt-emulation mechanism for flash-

memory storage systems with an energy-efficient management strategy. We prop ...

**Keywords:** embedded systems, energy-efficient, flash memory, interrupt-emulation I/O, programmed I/O, storage systems

#### 4 The end of the CRT?



Bob Myers

May 1997 **ACM SIGGRAPH Computer Graphics**, Volume 31 Issue 2

**Publisher:** ACM Press

Full text available: pdf(317.09 KB) Additional Information: [full citation](#), [abstract](#), [index terms](#)

Several technologies are becoming serious challengers to the time-honored cathode ray tube (CRT) as the display of choice for desktop use. This shift alone will have an impact on graphics hardware and software, but there are potentially greater changes on the horizon as these displays move off the desktop. This article examines the transition away from the CRT display, the changes this transition will demand from the rest of the computer system and what might be enabled by these new technologies ...

#### 5 A Solution for Hardware Emulation of Non Volatile Memory Macrocells

Alessandro Pirola

March 2003 **Proceedings of the conference on Design, Automation and Test in Europe: Designers' Forum - Volume 2 DATE '03**

**Publisher:** IEEE Computer Society

Full text available: pdf(98.58 KB) Additional Information: [full citation](#), [abstract](#), [index terms](#)  
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More and more the system verification makes use of hardware emulation techniques that allow a speed up in simulation performance up to thousand times. Typically, a design is composed by several parts, most of them are available as RTL code, other, mainly memories, only like behavioral models. In this scenario coemulation is necessary to verify the heterogeneous system descriptions, but this way most of the advantage of hardware emulation is lost. This paper presents a solution for modeling the a ...

#### 6 Can shared-memory model serve as a bridging model for parallel computation?



Phillip B. Gibbons, Yossi Matias, Vijaya Ramachandran

June 1997 **Proceedings of the ninth annual ACM symposium on Parallel algorithms and architectures**

**Publisher:** ACM Press

Full text available: pdf(1.62 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

#### 7 MemorIES3: a programmable, real-time hardware emulation tool for multiprocessor server design



Ashwini Nanda, Kwok-Ken Mak, Krishnan Sugarvanam, Ramendra K. Sahoo, Vijayaraghavan Soundararajan, T. Basil Smith

November 2000 **ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer Architecture News , Proceedings of the ninth international conference on Architectural support for programming languages and operating systems ASPLOS-IX**, Volume 34 , 28 Issue 5 , 5

**Publisher:** ACM Press

Full text available: pdf(724.53 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Modern system design often requires multiple levels of simulation for design validation and performance debugging. However, while machines have gotten faster, and simulators

have become more detailed, simulation speeds have not tracked machine speeds, As a result, it is difficult to simulate realistic problem sizes and hardware configurations for a target machine. Instead, researchers have focussed on developing scaling methodologies and running smaller problem sizes and configurations that atte ...


#### 8 Trace-driven memory simulation: a survey



Richard A. Uhlig, Trevor N. Mudge

June 1997 **ACM Computing Surveys (CSUR)**, Volume 29 Issue 2

**Publisher:** ACM Press

Full text available:  [pdf\(636.11 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

As the gap between processor and memory speeds continues to widen, methods for evaluating memory system designs before they are implemented in hardware are becoming increasingly important. One such method, trace-driven memory simulation, has been the subject of intense interest among researchers and has, as a result, enjoyed rapid development and substantial improvements during the past decade. This article surveys and analyzes these developments by establishing criteria for evaluating trac ...

**Keywords:** TLBs, caches, memory management, memory simulation, trace-driven simulation

#### 9 Randomized instruction set emulation



Elena Gabriela Barrantes, David H. Ackley, Stephanie Forrest, Darko Stefanović

February 2005 **ACM Transactions on Information and System Security (TISSEC)**, Volume 8 Issue 1

**Publisher:** ACM Press

Full text available:  [pdf\(374.44 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Injecting binary code into a running program is a common form of attack. Most defenses employ a "guard the doors" approach, blocking known mechanisms of code injection. *Randomized instruction set emulation* (RISE) is a complementary method of defense, one that performs a hidden randomization of an application's machine code. If foreign binary code is injected into a program running under RISE, it will not be executable because it will not know the proper randomization. The pape ...

**Keywords:** Automated diversity, randomized instruction sets, software diversity

#### 10 Emulation of the IBM system/360 on a microprogrammable computer



George R. Trimble

September 1974 **Conference record of the 7th annual workshop on Microprogramming**

**Publisher:** ACM Press

Full text available:  [pdf\(821.51 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

In recent months, several microprogrammable computers (MCs) have become available. By development of a suitable microprogram, it is possible to extend the basic instruction set of an MC to include special purpose instructions designed to facilitate the processing of a frequently executed application. This paper presents the results of a study of the technical and economic feasibility of the development of an S/360 emulator on an MC. It was concluded that such an emulator could be ...

#### 11 Emulation - a useful tool in the development of computer systems



F. A. Salomon, D. A. Tafuri

March 1982 **Proceedings of the 15th annual symposium on Simulation**

Full text available:  [pdf\(749.55 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Emulation is playing a key role in the development of a BELLMAC-32A microprocessor-based computer system at Bell Telephone Laboratories. The emulation is used for the development of the operating system for the new computer system to permit both hardware and software development to proceed in parallel. The emulation's goal is to permit the operating system to be developed before the hardware is available. This is aimed at reducing the time and effort required in the hardware/software integr ...

## 12 [Distributed applications, distributed memory, and internet applications: Byzantine disk](#)



### [paxos: optimal resilience with byzantine shared memory](#)

Ittai Abraham, Gregory V. Chockler, Idit Keidar, Dahlia Malkhi

July 2004 **Proceedings of the twenty-third annual ACM symposium on Principles of distributed computing**

**Publisher:** ACM Press

Full text available:  [pdf\(253.82 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We present Byzantine Disk Paxos, an asynchronous shared-memory consensus protocol that uses a collection of  $n > 3t$  disks,  $t$  of which may fail by becoming non-responsive or arbitrarily corrupted. We give two constructions of this protocol; that is, we construct two different building blocks, each of which can be used, along with a leader oracle, to solve consensus. One building block is a shared *wait-free* safe register. The second building block is a regular register ...

**Keywords:** Byzantine failures, consensus, shared-memory emulations, termination conditions


## 13 [Exploiting FPGA-features during the emulation of a fast reactive embedded system](#)



Karlheinz Weiß, Thorsten Steckstor, Gernot Koch, Wolfgang Rosenstiel

February 1999 **Proceedings of the 1999 ACM/SIGDA seventh international symposium on Field programmable gate arrays**

**Publisher:** ACM Press

Full text available:  [pdf\(2.02 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

## 14 [Improving Scalability of Network Emulation through Parallelism and Abstraction](#)

Cameron Kiddle, Rob Simmonds, Brian Unger

April 2005 **Proceedings of the 38th annual Symposium on Simulation**

**Publisher:** IEEE Computer Society

Full text available:  [pdf\(208.63 KB\)](#) Additional Information: [full citation](#), [abstract](#)

One approach to network emulation involves simulating a virtual network with a real-time network simulator and providing an I/O interface that enables interaction between real hosts and the virtual network. This allows real protocols and applications to be tested in a controlled and repeatable environment. To reflect conditions of large networks such as the Internet it is important that the emulation environment be scalable. This paper examines improvements in scalability of the virtual network ...

**Keywords:** Scalable Network Emulation, Parallel Discrete Event Simulation, Simulation Abstraction, Fluid Simulation

## 15 [Microprogramming and its relationship to emulation and technology](#)

Samuel H. Fuller, Victor R. Lesser, C. Gordon Bell, Charles Kaman

September 1974 **Conference record of the 7th annual workshop on Microprogramming**

**Publisher:** ACM Press

Full text available: pdf(723.27 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper is a survey of the development and future trends in microprogramming. We show how the structure of microprogrammed processors has been shaped primarily by two factors: the state of (semiconductor) technology and the task of emulation. The other main theme of this article is that it is a fruitless exercise to try to characterize and understand microprogramming in terms of how it differs from 'regular' programming. The right approach to understanding microprogramming is to recognize ...

## 16 Session 1A: Dynamic verification: Static scheduling of multi-domain memories for functional verification



Murali Kudluga, Charles Selvidge, Russell Tessier

November 2001 **Proceedings of the 2001 IEEE/ACM international conference on Computer-aided design****Publisher:** IEEE Press

Full text available: pdf(113.07 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Over the past decade both the quantity and complexity of available on-chip memory resources have increased dramatically. In order to ensure accurate ASIC behavior, both logic functions and memory resources must be successfully verified before fabrication. Often, the functional verification of contemporary ASIC memory is complicated by the presence of multiple design clocks that operate asynchronously to each other. The presence of multiple clock domains presents significant challenges for large ...

## 17 Preprototyping SIMD coprocessors using virtual machine emulation and trace compilation



Martin C. Herbordt, Owais Kidwai, Charles C. Weems

June 1997 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1997 ACM SIGMETRICS international conference on Measurement and modeling of computer systems SIGMETRICS '97**, Volume 25 Issue 1**Publisher:** ACM Press

Full text available: pdf(2.05 MB)

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The use of massively parallel SIMD array architectures is proliferating in the area of domain specific coprocessors. Even so, they have undergone few systematic empirical studies. The underlying problems include the size of the architecture space, the lack of portability of the test programs, and the inherent complexity of simulating up to hundreds of thousands of processing elements. We address the computational cost problem with a novel approach to trace-based simulation. Code is run on an abs ...

## 18 System description of the JHU emulation laboratory



Charles Neuhauser

September 1974 **Conference record of the 7th annual workshop on Microprogramming****Publisher:** ACM Press

Full text available: pdf(384.41 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The emulation laboratory described in this paper is being implemented at Johns Hopkins University as a research and educational facility. Currently, we are planning to use this laboratory to support research in the following areas: 1) Examination of and experimentation with novel system architectures, 2) Evaluation of various directly executed language (DEL) structures and the effective structure of their associated base machines [4], and 3) Dynamic analysis ...

## 19 Emulation of computer networks by microprogrammable microcomputers







David Cohen, Ming T. Liu

September 1974 **Conference record of the 7th annual workshop on Microprogramming****Publisher:** ACM PressFull text available: [pdf\(430.27 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The advent of low cost, sophisticated, microprogrammable, LSI microprocessors has renewed interest in multiple-computer systems. This paper suggests a method of implementing microprogrammable microcomputer systems as a sophisticated tool (emulators) for decreasing the economic risk involved in development of large computer networks. Two levels of emulation are proposed for different network configurations. At the first level each microprocessor emulates one of the large computers in the rea ...

**20** Emulation as a design tool in the development of real-time control systems

Howard M. Bloom, Cita M. Furlani, Anthony J. Barbera

January 1984 **Proceedings of the 16th conference on Winter simulation****Publisher:** IEEE PressFull text available: [pdf\(715.06 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A major facility for manufacturing research is being established at the National Bureau of Standards. The Automated Manufacturing Research Facility (AMRF) will provide testbed where measurement research of computer integrated manufacturing systems can be performed. The control architecture of the facility is based on a sensory-interactive, modular, hierarchical, feedback system. Each module is represented as a finite state machine that interacts through a shared time-sliced common-memory wh ...

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JA Sanchez-P, K Pramataris, N Vogiatzis - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

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### WIRELESS HOME NETWORKING

D Hepper, O Klank - [ifn.ing.tu-bs.de](http://ifn.ing.tu-bs.de)

... these: from TV-based to **PC**-based multimedia ... Whether **infrared** transmission can be an alternative or ... 2. **Bluetooth** Special Interest Group, 1999: [http:// www ...](http://www...)

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### HomeRF: wireless networking for the connected home

KJ Negus, AP Stephens, J Lansford - IEEE Personal Communications, 2000 - [cs.cmu.edu](http://cs.cmu.edu)

... CP USB **PC** ... **Bluetooth** 1 Mb/s Medium Via PPP Via IP & Cellular < 10 m < 1 mA ... it is still very expensive compared to the very low cost of **Infrared** Data Association ...

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### The Design And Implementation Of HomeRF: A Radio Frequency Wireless Networking Standard For The ...

J Lansford, P Bahl - Proceedings of the IEEE, 2000 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

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... Other complimentary network technologies, such as **Bluetooth**, JetSend, and ... pixels) and conditions of eight **infrared** sensors where ... of the system the **PC** an SUN ...

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### The design and technological feasibility of home systems for the elderly

C Tweed, G Quigley - The Queens University, Belfast, 2000 - [qub.ac.uk](http://qub.ac.uk)

... 10 4.1.1 **PC** processors ... 24 5.3 FireWire (**IEEE 1394**).... 25 5.4 ...

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## A MOBILE CODE APPROACH TO INTERACT WITH UBIQUITOUS EMBEDDED SYSTEMS

K Kangas, J Roening - student.oulu.fi

... communication devices, most notably the upcoming **Bluetooth** [7], will ... the network capabilities of the **IEEE 1394** bus. ... For example, a **PC** connected to the HAVi ...

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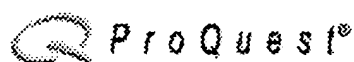
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*Rev. Lynn Watson. The Ottawa Citizen.* Ottawa, Ont.: May 6, 2004. p. B.4  
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*William Lynn Watson. College Literature.* West Chester: Fall 2002. Vol. 29, Iss. 4; p. 6 (20 pages)

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*Sam G Campbell, Mary-Lynn Watson. CJEM : Journal of the Canadian Association of Emergency Phy*  
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*WARREN E WATSON, LYNN JOHNSON, DEANNA MERRITT. Group & Organization Studies (1986-19*  
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*LYNN WATSON Occidental. The Press Democrat.* Santa Rosa, Calif.: Dec 22, 1995. p. B.6

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A Racz, G Miklos, F Kubinszky, A Valko - ACM Symposium on Mobile **AD Hoc** Networking and Computing - portal.acm.org

... Andras Racz, Gyorgy Miklos, Ferenc Kubinszky, Andras Valko Traffic Analysis and ... allows handheld devices to be rapidly interconnected into **ad hoc** networks. ...

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### A Bluetooth scatternet-route structure for multihop **ad hoc** networks

Y Liu, MJ Lee, TN Saadawi - IEEE Journal on Selected Areas in Communications, 2003 - [ieeexplore.ieee.org](#)

... Multihop **Ad Hoc** Networks ... Index Terms—**Ad hoc** networks, medium access control, on-de-mand routing, scatternet formation, scatternet scheduling. ...

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### Emulation of **ad-hoc** networks on IEEE 802.11

F Kubinszky, Z Lzr, A Varga, B Eged, A Telegdy - Proceedings of MIKON 2000 Conference, May, 2000 - [wit.mht.bme.hu](#)

Page 1. Budapest University of Technology and Economics Emulation of **ad-hoc** networks on IEEE 802.11 Ferenc Kubinszky Master of Science Thesis ...

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### End-to-end bandwidth guarantees through fair local spectrum share in wireless **ad-hoc** networks

S Sarkar, L Tassiulas - Control and Decision Conference (CDC) - [ieeexplore.ieee.org](#)

... and Control Maw, Hawaii USA, December 2003 TuMO3-1 End-to-end bandwidth guarantees through fair local spectrum share in wireless **ad-hoc** networks Saswati Sarkar ...

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### Performance issues of Bluetooth scatternets and other asynchronous TDMA **ad hoc** networks

T Salonidis, L Tassiulas - Proc. International Workshop on Mobile Multimedia ... - [ece.umd.edu](#)

... other asynchronous TDMA **ad hoc** networks ... Optimal link scheduling in wireless **ad hoc** networks has been studied by [2] [3] for various interference constraints. ...

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T Salonidis, L Tassiulas - Proc. IEEE International Workshop on Quality of Service ( ... - [ieeexplore.ieee.org](#)

Page 1. Distributed on-line schedule adaptation for balanced slot allocation in wireless **ad hoc** networks Theodoros Salonidis † and Leandros Tassiulas †,‡ ...

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### A simple neighbour discovery procedure for Bluetooth **ad hoc** networks

MA Ronai, E Kail - Proceedings of Globecom, 2003 - [ieeexplore.ieee.org](#)

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M Dideles - Crossroads, 2003 - [portal.acm.org](#)

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5	BRS	L5	0	(multiple adj emulat\$.ti. and serial and bus and memory	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 14:55
6	BRS	L6	0	(multiple adj emulat\$.ti. and serial and bus and memory and wireless	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 14:55
7	BRS	L7	0	"5920712".pn. and wireless	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 14:56
8	BRS	L8	0	"5920712".pn. and ethernet	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 14:56

	Type	L #	Hits	Search Text	DBs	Time Stamp
9	BRS	L9	1	"6968307".pn. and ethernet	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 15:07
10	BRS	L10	7	("802.11b" same emulation)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 15:07
11	BRS	L11	0	("802.11b" same emulation) and (emulation same multiple)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 15:08
12	BRS	L12	0	("802.11b" same emulation) and (emulation near multiple)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 15:08
13	BRS	L13	7	("802.11b" same emulation) and (emulation near/2 multiple)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 15:08
14	BRS	L14	0	("802.11b" same emulation) and 703/24.ccls.	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 15:09
15	BRS	L15	1	"802.11b" and 703/24.ccls.	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 15:09
16	BRS	L16	82	"802.11" and emulation	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 15:15

	Type	L #	Hits	Search Text	DBs	Time Stamp
17	BRS	L17	0	"802.11" and (emulator\$ same multiple)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 15:15
18	BRS	L18	1	"802.11" and (emulator\$ same multiple)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/30 15:15
19	BRS	L19	240	703/25.ccls.	USPAT	2005/12/30 15:44



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